

Notice of References Cited			Application/Control No.	Applicant(s)/Patent Under Reexamination	
			10/071,373	HOWARD ET AL.	
			Examiner	Art Unit	Page 1 of 1
			Chat C. Do	2124	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,305,248	04-1994	Ammann, Lawrence M.	708/502
	B	US-5,226,001	07-1993	Garverick, Steven L.	708/313
	C	US-4,048,485	09-1977	Nussbaumer, Henri J.	708/421
	D	US-2002/0194239	12-2002	Pangal, Amaresh	708/498
	E	US-6,480,872	11-2002	Choquette, Jack H.	708/501
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Shin-ichi et al., A 100-MHz 2-D discrete cosine transform core processor, April 1992, IEEE Journal of Solid State Circuits, pages 492-499.
	V	Chip et al., The coreware methodology: building a 200 Mflop processor in 9 man months, September 1992, IEEE, pages 549-552.
	W	Debabrata et al., A 600 MHz half-bit level pipelined accumulator-interleaved multiplier accumulator core, October 1993, IEEE, pages 498-506.
	X	Emad et al., A programmable power-efficient decimation filter for software radios, August 1997, IEEE, pages 68-71.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.